## Applicant(s)/Patent Under Application/Control No. Reexamination 09/519,187 ZINGER ET AL. Notice of References Cited Art Unit Examiner Page 1 of 1 2134 David Y Jung **U.S. PATENT DOCUMENTS** Document Number Date Classification Name Country Code-Number-Kind Code MM-YYYY 713/194 08-2004 Williams et al. US-6,782,479 713/194 US-6,775,778 08-2004 Laczko et al. В 713/200 US-6,771,446 08-2004 Noble, Gayle L. С D US-US-Ε US-F US-G US-Н US-US-J US-Κ US-L US-М FOREIGN PATENT DOCUMENTS Date **Document Number** Country Name Classification Country Code-Number-Kind Code MM-YYYY Ν O Ρ Q R S Т **NON-PATENT DOCUMENTS** Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) Ze et al., A research on system-level architecture of single-chip cryptographic data processing, Communications, Circuits and Systems and West Sino Expositions, IEEE 2002, International Conference on, Volume 2, 29 June-1 July 2002, pp. 1486-1490 McLoone et al., A single-chip IPSEC cryptograhic processor, Signal Processing Systems, 2002, IEEE Workship on, 16-18 Oct. 2002, pp. 133-138 DuPont et al., Robustness IPs for reliability and security of SoCs, Test Conference, 2002,. Proceedings, Interenational, 7-10 W October 2002, pp. 357-364

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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